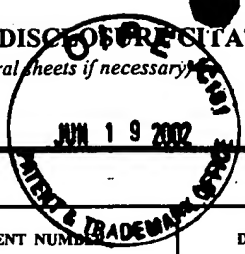


INFORMATION DISCLOSURE CITATION
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Docket Number (Optional)

03226.053002; P5

Application Number

09/904,884

Applicant(s)

Ronald J. MELANSON, et al.

Filing Date

July 12, 2001

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2133

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SAC	A1	5,916,314	06/29/99	Thomas B. Berg, et al.	714	6	09/11/96

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FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

SAC	A2	International Search Report, dated May 28, 2002 (7 pages).
SAC	A3	700 IBM Technical Disclosure Bulletin, Vol. 31, No. 3, August 1988, Armonk, NY (4 pages).

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DATE CONSIDERED

11-5-03

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INITIAL

(Including Author, Title, Date, Pertinent Pages, Etc.)

P.R. Turgeon, et al., "Two Approaches to Array Fault Tolerance in the IBM Enterprise System/9000 Type 9121 Processor", IBM Journal of Research and Development, Vol. 35, No. 3, May 1991, Armonk, NY (7 pages).

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